

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Jeffrey Lukanc

Assignee: Integrated Device Technology, Inc.

Title: TEST BUS ARCHITECTURE FOR EMBEDDED RAM AND METHOD  
OF OPERATING SAME

Serial No: 09/887,913

Group No.: 2133

Filed: 6/20/2001

Examiner: Guy Lamarre

Attorney Docket No: IDT-1616

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December 14, 2006

Mail Stop Amendment  
Commissioner of Patents and Trademarks  
P.O. Box 1450  
Alexandria, VA 22313-1450AMENDMENT IN RESPONSE TO FIFTH OFFICE ACTION

Sir:

Applicant submits the following amendments and remarks in response to the Office Action dated June 16, 2006. A Request for Extension of Time, extending the period for response to December 18, 2006, accompanies the present response.